# On the Expressiveness of Forwarding in Higher-Order Communication (May 2, 2009)

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**Abstract.** In higher-order process calculi the values exchanged in communications may contain processes. There are only two capabilities for received processes: execution and forwarding. Here we propose a limited form of forwarding: output actions can only communicate the parallel composition of statically known closed processes and processes received through previously executed input actions. We study the expressiveness of a higher-order process calculus featuring this style of communication. Our main result shows that in this calculus termination is decidable while convergence is undecidable.

#### 1 Introduction

Higher-order process calculi are calculi in which processes can be communicated. They have been put forward in the early 1990s, with CHOCS [1], Plain CHOCS [2], the Higher-Order  $\pi$ -calculus [3], and others. Higher-order (or process-passing) concurrency is often presented as an alternative paradigm to the first order (or name-passing) concurrency of the  $\pi$ -calculus for the description of mobile systems. These calculi are inspired by, and formally closer to the  $\lambda$ -calculus, whose basic computational step —  $\beta$ -reduction — involves term instantiation. As in the  $\lambda$ -calculus, a computational step in higher-order calculi results in the instantiation of a variable with a term, which is then copied as many times as there are occurrences of the variable.

HOCORE is a core calculus for higher-order concurrency, recently introduced in [4]. It is *minimal*, in that only the operators strictly necessary to obtain higher-order communications are retained. For instance, continuations following output messages have been left out. More importantly, HOCORE has no restriction operator. Thus all channels are global, and dynamic creation of new channels is impossible. This makes the absence of recursion also relevant, as known encodings of fixed-point combinators in higher-order process calculi require the restriction operator. The grammar of HOCORE processes is:

$$P ::= a(x). P \mid \overline{a}\langle P \rangle \mid P \parallel P \mid x \mid \mathbf{0} \qquad (*$$

An input prefixed process a(x). P can receive on name (or channel) a a process that will be substituted in the place of x in the body P; an output message  $\overline{a}\langle P\rangle$  can send P (the output object) on a; parallel composition allows processes to interact.

Despite this minimality, via a termination preserving encoding of Minsky machines [5], HOCORE was shown to be Turing complete. Therefore, in HOCORE, properties such as *termination* (i.e. non existence of divergent computations) and *convergence* (i.e.

existence of a terminating computation)<sup>1</sup> are both undecidable. In contrast, somewhat surprisingly, strong bisimilarity is decidable, and several sensible bisimilarities in the higher-order setting coincide with it.

In this paper, we shall aim at identifying the intrinsic source of expressive power in HOCORE. A substantial part of the expressive power of a concurrent language comes from the ability of accounting for infinite behavior. In higher-order process calculi there is no explicit operator for such a behavior, as both recursion and replication can be encoded. We then find that infinite behavior resides in the interplay of higher-order communication, in particular, in the ability of forwarding a received process within an arbitrary context. For instance, consider the process  $R = a(x). \bar{b}\langle P_x \rangle$  (here  $P_x$ stands for a process P with free occurrences of a variable x). Intuitively, R receives a process on name a and forwards it on name b. It is easy to see that since objects in output actions are built following the syntax given by (\*), the actual structure of  $P_x$ can be fairly complex. One could even "wrap" the process to be received in x using an arbitrary number of k "output layers", i.e., by letting  $P_x \equiv \overline{b_1} \langle \overline{b_2} \langle \dots \overline{b_k} \langle x \rangle \rangle \dots \rangle$ . This nesting capability embodies a great deal of the expressiveness of HOCORE: as a matter of fact, the encoding of Minsky machines in [4] depends critically on nesting-based counters. Therefore, investigating suitable limitations to the kind of processes that can be communicated in an output action appears as a legitimate approach for assessing the expressive power of higher-order concurrency.

With the above consideration in mind, in this paper we propose HO<sup>-f</sup>, a sublanguage of HOCORE in which output actions are limited so as to rule out the nesting capability. In HO<sup>-f</sup>, output actions can communicate the parallel composition of two kinds of objects: (i) statically known closed processes (i.e. that do not contain free variables), and (ii) processes received through previously executed input actions. Hence, the context in which the output action resides can only contribute to communication by "appending" pieces of code that admit no inspection, available in the form of a black-box. More formally, the grammar of HO<sup>-f</sup> processes is that in (\*), excepting the production for output actions, which is replaced by the following one:

$$\overline{a}\langle x_1 \parallel \cdots \parallel x_k \parallel P \rangle$$

where  $k \ge 0$  and P is a closed process. This modification directly restricts forwarding capabilities for output processes, which in turn, leads to a more limited structure of processes along reductions.

The limited style of higher-order communication enforced in  $\operatorname{HO}^{-f}$  is relevant from a pragmatic perspective. In fact, communication in  $\operatorname{HO}^{-f}$  is inspired by those cases in which a process P is communicated in a translated format  $[\![P]\!]$ , and the translation is not compositional. That is, the cases in which, for any process context C, the translation of C[P] cannot be seen as a function of the translation of P, i.e. there exists no context P such that  $[\![C[P]]\!] = P[P]$ . This setting can be related to several existing programming scenarios. The simplest example is perhaps mobility of already compiled code, on which it is not possible to apply inverse translations (such as reverse engineering). Other examples include  $Proof-carrying\ code\ [6]$  and communication of P observed the set of P of P is the set of P of P of P and P of P of

<sup>&</sup>lt;sup>1</sup> Termination and convergence are sometimes also referred to as *universal* and *existential* termination, respectively.

[7]. The former features communication of executable code that comes with a certificate: a recipient can only check the certificate and decide whether to execute the code or not. The latter consists in the communication of source code that is made difficult to understand for, e.g., security/copyright reasons, while preserving its functionality.

The main contribution of the paper is the study of the expressiveness of Ho<sup>-f</sup> in terms of decidability of termination and convergence. Our main results are:

- 1. Similarly as HOCORE, HO<sup>-f</sup> is Turing complete. Therefore, despite the limitation of forward capabilities motivated above, the calculus has a significant expressive power. The result is obtained by exhibiting an encoding of Minsky machines.
- 2. In sharp contrast with HOCORE, termination in HO<sup>-f</sup> is *decidable*. This result is obtained by appealing to the theory of well-structured transition systems [8], following the approach used in [9].

As for (1), it is worth commenting that the encoding is not *faithful* in the sense that, unlike the encoding of Minsky machines in HOCORE, it may introduce computations which do not correspond to the expected behavior of the modeled machine. Such computations are forced to be infinite and thus regarded as non-halting computations which are therefore ignored. Only the finite computations correspond to those of the encoded Minsky machine. This way, we prove that a Minsky machine terminates if and only if its encoding in HO<sup>-f</sup> converges. Consequently, convergence in HO<sup>-f</sup> is *undecidable*.

As for (2), the use of the theory of well-structured transition systems is certainly not a new approach for obtaining expressiveness results. However, to the best of our knowledge, this is the first time it is applied in the higher-order setting. This is significant because the adaptation to the Ho<sup>-f</sup> case is far from trivial. Indeed, as we shall discuss, this approach relies on approximating an upper bound on the depth of the (set of) derivatives of a process. By *depth* of a process we mean its maximal nesting of input/output actions. Notice that, even with the limitation on forwarding enforced by Ho<sup>-f</sup>, because of the "term copying" feature of higher-order calculi, variable instantiation might lead to a potentially larger process. Hence, finding suitable ways of bounding the set of derivatives of a process is rather challenging and needs care.

**Structure of the paper.** The syntax and semantics of Ho<sup>-f</sup> are introduced in Section 2. The encoding of Minsky machines into Ho<sup>-f</sup>, and the undecidability of convergence are discussed in Section 3. The decidability of termination is addressed in Section 4. Some final remarks, as well as a review of related work, are included in Section 5.

## 2 The Calculus

We now introduce the syntax and LTS of  ${\rm Ho}^{-{\rm f}}$ . We use a,b,c to range over names (also called channels), and x,y,z to range over variables; the sets of names and variables are disjoint.

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\begin{array}{lll} P,\,Q ::= \overline{a} \langle x_1 \parallel \cdots \parallel x_k \parallel P \rangle & \text{(with } k \geq 0, \text{ fv}(P) = \emptyset) & \text{output} \\ & \mid \ a(x).\,P & & \text{input prefix} \\ & \mid \ P \parallel Q & & \text{parallel composition} \\ & \mid \ x & & \text{process variable} \\ & \mid \ \mathbf{0} & & \text{nil} \end{array}
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An input a(x). P binds the free occurrences of x in P. We write  $\operatorname{fv}(P)$  for the set of free variables in P, and  $\operatorname{bv}(P)$  for the bound variables. A process is  $\operatorname{closed}$  if it does not have free variables. We abbreviate a(x). P, with  $x \notin \operatorname{fv}(P)$ , as a. P,  $\overline{a}\langle \mathbf{0}\rangle$  as  $\overline{a}$ , and  $P_1 \parallel \ldots \parallel P_k$  as  $\prod_{i=1}^k P_i$ . Hence, an output action can be written as  $\overline{a}\langle \prod_{k \in K} x_k \parallel P \rangle$ . Similarly, we write  $\prod_{i=1}^n P$  as an abbreviation for the parallel composition of n copies of P. Further,  $P\{Q/x\}$  denotes the substitution of the free occurrences of x with process Q in P.

Now we describe the Labeled Transition System, which is defined on closed processes. There are three forms of transitions:  $\tau$  transitions  $P \xrightarrow{\tau} P'$ ; input transitions  $P \xrightarrow{a(x)} P'$ , meaning that P can receive at a a process that will replace x in the continuation P'; and output transitions  $P \xrightarrow{\overline{a}\langle P'\rangle} P''$  meaning that P emits P' at a, and in doing so it evolves to P''. We use  $\alpha$  to indicate a generic label of a transition.

(We have omitted ACT2 and TAU2, the symmetric counterparts of the last two rules.)

Remark 1. Since we consider closed processes, in rule ACT1,  $P_2$  has no free variables and no side conditions are necessary. As a consequence, alpha-conversion is not needed.

**Definition 1.** The structural congruence relation is the smallest congruence generated by the following laws:

$$P \parallel \mathbf{0} \equiv P, P_1 \parallel P_2 \equiv P_2 \parallel P_1, P_1 \parallel (P_2 \parallel P_3) \equiv (P_1 \parallel P_2) \parallel P_3.$$

Reductions  $P \longrightarrow P'$  are defined as  $P \xrightarrow{\tau} P'$ . We now state a few results which will be important later.

**Lemma 1.** If  $P \xrightarrow{\alpha} P'$  and  $P \equiv Q$  then there exists Q' such that  $Q \xrightarrow{\alpha} Q'$  and  $P' \equiv Q'$ .

*Proof.* By induction on the derivation of  $P \equiv Q$ , then by case analysis on  $P \xrightarrow{\alpha} Q$ .

The *alphabet* of an HO<sup>-f</sup> process is defined as follows:

**Definition 2** (Alphabet of a process). Let P be a  $Ho^{-f}$  process. The alphabet of P, denoted A(P), is inductively defined as:

$$\mathcal{A}(\mathbf{0}) = \emptyset \qquad \qquad \mathcal{A}(P \parallel Q) = \mathcal{A}(P) \cup \mathcal{A}(Q) \qquad \qquad \mathcal{A}(x) = \{x\}$$
$$\mathcal{A}(a(x), P) = \{a, x\} \cup \mathcal{A}(P) \qquad \qquad \mathcal{A}(\overline{a}\langle P \rangle) = \{a\} \cup \mathcal{A}(P)$$

**Proposition 1.** Let P be a  $Ho^{-f}$  process. The set A(P) is finite.

*Proof.* Straightforward from Definition 2, exploiting the fact that in  $Ho^{-f}$  alpha-conversion is not necessary (see Remark 1).

**Proposition 2.** Let P and P' be  $HO^{-f}$  processes. If  $P \xrightarrow{\alpha} P'$  then  $\mathcal{A}(P') \subseteq \mathcal{A}(P)$ .

*Proof.* We proceed by a case analysis on the rule used to infer  $\stackrel{\alpha}{\longrightarrow}$ . We thus have six cases:

Case INP Then  $P \equiv a(x)$ . P'. By Definition 2,  $\mathcal{A}(P) = \{a, x\} \cup \mathcal{A}(P')$ , and the thesis holds.

**Case OUT** Then  $P \equiv \overline{a}\langle Q \rangle$ . By Definition 2  $\mathcal{A}(P) = \{a\} \cup \mathcal{A}(Q)$  and  $\mathcal{A}(P') = \emptyset$ , and the thesis holds.

Case TAU1 Then  $P = P_1 \parallel P_2$ ,  $P' = P'_1 \parallel P'_2\{R/x\}$ , with  $P_1 \xrightarrow{\overline{a}\langle R \rangle} P'_1$  and  $P_2 \xrightarrow{a(x)} P'_2\{R/x\}$ . By Definition 2 we have that  $\mathcal{A}(P_1) = \{a\} \cup \mathcal{A}(P'_1) \cup \mathcal{A}(R)$  and hence  $\mathcal{A}(P'_1) \subseteq \mathcal{A}(P_1)$ . Also by Definition 2 we have  $\mathcal{A}(P_2) = \{a,x\} \cup \mathcal{A}(P'_2)$ . Now, the process R is closed: therefore, during substitution, no variable can be captured. Hence,  $\alpha$ -conversion is not needed, and we have  $\mathcal{A}(P'_2\{R/x\}) \subseteq \mathcal{A}(P'_2) \cup \mathcal{A}(R)$ . The result then follows.

Case TAU2 Similarly as for TAU1.

**Case ACT1** Then  $P \equiv P_1 \parallel P_2$ ,  $P' \equiv P'_1 \parallel P_2$ , and  $P_1 \xrightarrow{\alpha} P'_1$ . We then have  $\mathcal{A}(P'_1) \subseteq \mathcal{A}(P_1)$  by using one of the above cases. By noting that  $\mathcal{A}(P'_1) \cup \mathcal{A}(P_2) \subseteq \mathcal{A}(P_1) \cup \mathcal{A}(P_2)$ , the thesis holds.

Case ACT2 Similarly as for ACT1.

Remark 2. Had we considered open processes, we would have required  $\alpha$ -conversion. In such a case,  $\mathcal{A}(P_2'\{R/x\}) \subseteq \mathcal{A}(P_2') \cup \mathcal{A}(R)$  would no longer hold. This is because by using  $\alpha$ -conversion during substitution some new variables could be added to the alphabet.

Given a process P, its internal runs  $P\longrightarrow P_1\longrightarrow P_2\longrightarrow \dots$  are given by sequences of reductions. We denote with  $\longrightarrow^*$  the reflexive and transitive closure of  $\longrightarrow$ ; notation  $\longrightarrow^j$  is to stand for a sequence of j reductions. We use  $P \nrightarrow$  to denote that there is no P' such that  $P\longrightarrow P'$ . Following [9] we now define process convergence and process termination.

**Definition 3.** Let P be a  $Ho^{-f}$  process.

- 1. P converges iff there exists P' such that  $P \longrightarrow^* P'$  and  $P' \nrightarrow$ .
- 2. P terminates iff there exist no  $\{P_i\}_{i\in\mathbb{N}}$  such that  $P_0 = P$  and  $P_j \longrightarrow P_{j+1}$  for any j.

Observe that process termination implies process convergence while the opposite does not hold.

## 3 Convergence is Undecidable

In this section we show that HO<sup>-f</sup> is powerful enough to model Minsky machines [5], a Turing complete formalism. We present an encoding that is not *faithful*: unlike the encoding of Minsky machines in HOCORE, it may introduce computations which do not correspond to the expected behavior of the modeled machine. Such computations are forced to be infinite and thus regarded as non-halting computations which are therefore

$$\begin{split} \text{M-Inc} \ \frac{i: \text{Inc}(r_j) \quad m_j' = m_j + 1 \quad m_{1-j}' = m_{1-j}}{(i, m_0, m_1) \longrightarrow_{\text{M}} (i+1, m_0', m_1')} \\ \text{M-Dec} \ \frac{i: \text{Decj}(r_j, s) \quad m_j \neq 0 \quad m_j' = m_j - 1 \quad m_{1-j}' = m_{1-j}}{(i, m_0, m_1) \longrightarrow_{\text{M}} (i+1, m_0', m_1')} \\ \text{M-JMP} \ \frac{i: \text{Decj}(r_j, s) \quad m_j = 0}{(i, m_0, m_1) \longrightarrow_{\text{M}} (s, m_0, m_1)} \end{split}$$

Table 1. Reduction of Minsky machines

ignored. Only finite computations correspond to those of the encoded Minsky machine. More precisely, given a Minsky machine N, its encoding  $[\![N]\!]$  has a terminating computation if and only if N terminates. This allows to prove that convergence is undecidable.

We begin by briefly recalling the definition of Minsky machines; we then present the encoding into  $\mathrm{Ho}^{-f}$  and discuss its correctness.

**Minsky machines.** A Minsky machine is a Turing complete model composed of a set of sequential, labeled instructions, and two registers. Registers  $r_j$   $(j \in \{0,1\})$  can hold arbitrarily large natural numbers. Instructions  $(1:I_1),\ldots,(n:I_n)$  can be of two kinds:  $\mathrm{INC}(r_j)$  adds 1 to register  $r_j$  and proceeds to the next instruction;  $\mathrm{DECJ}(r_j,s)$  jumps to instruction s if  $r_j$  is zero, otherwise it decreases register  $r_j$  by 1 and proceeds to the next instruction. A Minsky machine includes a program counter p indicating the label of the instruction being executed. In its initial state, the machine has both registers set to 0 and the program counter p set to the first instruction. The Minsky machine stops whenever the program counter is set to a non-existent instruction, i.e. p > n. A configuration of a Minsky machine is a tuple  $(i, m_0, m_1)$ ; it consists of the current program counter and the values of the registers. Formally, the reduction relation over configurations of a Minsky machine, denoted  $\longrightarrow_{\mathsf{M}}$ , is defined in Table 1.

In the encoding of a Minsky machine into Ho<sup>-f</sup> we will find it convenient to have a simple form of guarded replication. This construct can be encoded in Ho<sup>-f</sup> as follows.

**Input-guarded replication.** We follow the standard encoding of replication in higher-order process calculi, adapting it to input-guarded replication so as to make sure that diverging behaviors are not introduced. As there is no restriction in Ho<sup>-f</sup>, the encoding is not compositional and replications cannot be nested.

**Definition 4.** Assume a fresh name c. The encoding of input-guarded replication is as follows:

$$\llbracket !a(z). P \rrbracket_{!!} = a(z). (Q_c \parallel P) \parallel \overline{c} \langle a(z). (Q_c \parallel P) \rangle$$

where  $Q_c = c(x).(x \parallel \overline{c}\langle x \rangle)$ , P contains no replications (nested replications are forbidden), and  $\llbracket \cdot \rrbracket_{i!}$  is an homomorphism on the other process constructs in  $Ho^{-f}$ .

The above encoding preserves termination.

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\begin{split} \text{REGISTER } r_j & & \llbracket r_j = m \rrbracket_{\mathsf{M}} = \prod_1^m \overline{u_j} \\ \text{INSTRUCTIONS } (i:I_i) \\ & \llbracket (i: \mathsf{INC}(r_j)) \rrbracket_{\mathsf{M}} & = !p_i. \, (\overline{u_j} \parallel set_j(x). \, \overline{set_j} \langle x \parallel \mathsf{INC}_j \rangle \parallel \overline{p_{i+1}}) \\ & \llbracket (i: \mathsf{DECJ}(r_j,s)) \rrbracket_{\mathsf{M}} = & !p_i. \, \overline{m_i} \\ & & \parallel !m_i. \, (\overline{loop} \parallel u_j. \, loop. \, set_j(x). \, \overline{set_j} \langle x \parallel \mathsf{DEC}_j \rangle \parallel \overline{p_{i+1}}) \\ & & \parallel !m_i. \, set_j(x). \, (x \parallel \overline{set_j} \langle \mathbf{0} \rangle \parallel \overline{p_s})) \end{split} where \mathsf{INC}_j = \overline{loop} \parallel check_j. \, loop \qquad \mathsf{DEC}_j = \overline{check_j} \\ & \mathsf{Table 2. \, Encoding \, of \, Minsky \, machines} \end{split}
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**Lemma 5 (Correctness of**  $[\![\cdot]\!]_{i!}$ ) Let P be a  $Ho^{-f}$  process with non-nested input-guarded replications.

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- If [\![P]\!]_{!!} \longrightarrow Q then \exists P' such that P \longrightarrow P' and either [\![P']\!]_{!!} = Q or Q \longrightarrow [\![P']\!]_{!!}.

- If P \longrightarrow P' then either [\![P]\!]_{!!} \longrightarrow [\![P']\!]_{!!} or [\![P]\!]_{!!} \longrightarrow [\![P']\!]_{!!}.

- [\![P]\!]_{!!} \nrightarrow iff P \nrightarrow.
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Proof. By induction on the transitions.

Encoding Minsky machines into  $\operatorname{HO}^{-f}$ . The encoding of Minsky machines into  $\operatorname{HO}^{-f}$  is denoted by  $\llbracket \cdot \rrbracket_{\mathsf{M}}$  and presented in Table 2. The encoding of a register  $r_j$  that stores the number m is the parallel composition of m copies of the unit process  $\overline{u_j}$ . To implement the test for zero it is necessary to record how many increments and decrements have been performed on the register  $r_j$ . This is done by using a special process  $\operatorname{LOG}_j$ , which is communicated back and forth on channel  $\operatorname{set}_j$ . More precisely, every time an increment instruction occurs, a new copy of the process  $\overline{u_j}$  is created, and the process  $\operatorname{LOG}_j$  is updated by adding the process  $\operatorname{INC}_j$  in parallel. Similarly for decrements: a copy of  $\overline{u_j}$  is consumed and the process  $\operatorname{DEC}_j$  is added to  $\operatorname{LOG}_j$ . As a result, after k increments and k decrements on register k, process k in form k increments and k decrements on register k, process k increments and k decrements on register k, process k increments and k decrements on register k, process k in k increments and k decrements on register k increments and k decrements on register k increments and k decrements on register k.

Each instruction  $(i:I_i)$  is a replicated process guarded by  $p_i$ , which represents the program counter when p=i. Once  $p_i$  is consumed, the instruction is active and an interaction with a register occurs. We already described the behavior of increments. Let us now focus on decrements, the instructions that can introduce divergent —unfaithful—computations. In this case, the process can internally choose either to actually perform a decrement and proceed with the next instruction, or to jump. This can be seen as a guess the process makes on the actual number stored by the register  $r_j$ . Therefore, two situations can occur:

1. The process chooses to decrement  $r_j$ . In this case instruction  $p_{i+1}$  is immediately enabled, and the process launches process  $\overline{loop}$  and then tries to consume a copy of  $\overline{u_j}$ . If this operation succeeds (i.e. the content of  $r_j$  is greater than 0) then a synchronization with the input on loop that guards the updating of  $Log_j$  (represented as an output on name  $set_j$ ) takes place. Otherwise, the unit process  $\overline{u_j}$  could not be consumed (i.e. the content of  $r_j$  is zero and the process made a wrong guess).

Process  $\overline{loop}$  then synchronizes with the external process loop. DIV, thus spawning a divergent computation.

2. The process chooses to jump to instruction  $p_s$ . In this case instruction  $p_s$  is immediately enabled, and it is necessary to check if the actual value stored by  $r_j$  is zero. To do so, the process receives the process  $LOG_j$  and launches it. If the number of increments is equal to the number of decrements then complementary signals on the name  $check_j$  will match each other. In turn, this allows each signal  $\overline{loop}$  executed by an  $INC_j$  process to be matched by a complementary one. Otherwise, it is then the case that at least one of those  $\overline{loop}$  signals remains active (i.e. the content of the register is not zero); a synchronization with the process loop. DIV then takes place, and a divergent computation is spawned.

Before executing the instructions, we require both registers in the Minsky machine to be set to zero. This is to guarantee correctness: starting with values different from zero in the registers (without proper initialization of the logs) can lead to inconsistencies. For instance, the test for zero would succeed (i.e. without spawning a divergent computation) even for a register whose value is different than zero. We now define the precise structure of the encoding of configurations.

**Definition 6 (Encoding of Configurations).** Let N be a Minsky machine with registers  $r_0$ ,  $r_1$  and instructions  $(1:I_1), \ldots, (n:I_n)$ . For  $j \in \{0,1\}$ , suppose fresh, pairwise different names  $r_j$ ,  $p_1, \ldots, p_n$ , set j, loop, check j. Given the encodings in Table 2, we have:

1. The initial configuration (1,0,0) of N is encoded as:

$$\llbracket (1,0,0) \rrbracket_{\mathsf{M}} ::= \overline{p_1} \parallel \prod_{i=1}^n \llbracket (i:I_i) \rrbracket_{\mathsf{M}} \parallel loop. \, \mathrm{DIV} \parallel \overline{set_0} \langle \mathbf{0} \rangle \parallel \overline{set_1} \langle \mathbf{0} \rangle \; .$$

2. A configuration  $(i, m_0, m_1)$  of N, after  $k_j$  increments and  $l_j$  decrements of register  $r_j$ , is encoded as:

$$[\![(i,m_0,m_1)]\!]_{\mathsf{M}} = \overline{p_i} \parallel [\![r_0 = m_0]\!]_{\mathsf{M}} \parallel [\![r_1 = m_1]\!]_{\mathsf{M}} \parallel \prod_{i=1}^n [\![(i:I_i)]\!]_{\mathsf{M}} \parallel \\ loop. \, \mathsf{DIV} \parallel \overline{set_0} \langle \mathsf{Log}_0[k_0,l_0] \rangle \parallel \overline{set_1} \langle \mathsf{Log}_1[k_1,l_1] \rangle$$

where DIV is a divergent process (e.g.  $\overline{w} \parallel !w. \overline{w}$ ).

We now formalize the following intuition: removing the program counter from the encoding of configurations leads to a stuck process.

**Proposition 3.** Let N be a Minsky machine with registers  $r_0$ ,  $r_1$  and instructions  $(1 : I_1), \ldots, (n : I_n)$ . Given the encodings in Table 2, let P be defined as:

$$P = [\![r_0 = m_0]\!]_{\mathsf{M}} \parallel [\![r_1 = m_1]\!]_{\mathsf{M}} \parallel \prod_{i=1}^n [\![(i:I_i)]\!]_{\mathsf{M}} \parallel \\ loop. \, \mathsf{DIV} \parallel \overline{set_0} \langle \mathsf{Log}_0[k_0, l_0] \rangle \parallel \overline{set_1} \langle \mathsf{Log}_1[k_1, l_1] \rangle \, .$$

Then  $P \rightarrow$ .

*Proof.* Straightforward by the following facts:

- 1. Processes  $[r_0 = m_0]_M$ ,  $[r_1 = m_1]_M$ ,  $\overline{set_0}\langle Log_0[k_0, l_0]\rangle$ , and  $\overline{set_1}\langle Log_1[k_1, l_1]\rangle$  are output actions that cannot evolve on their own.
- 2. Each  $[(i:I_i)]_M$  is an input-guarded process, waiting for an activation signal on  $p_i$ .
- 3. *loop*. DIV is an input-guarded process, and every output on *loop* appears guarded inside a decrement instruction.

The following notation will be useful in proofs.

**Notation 1** Let N be a Minsky machine. The configuration  $(i, m_0, m_1)$  of N is annotated as  $(i, m_0^{k_0, l_0}, m_1^{k_1, l_1})$ , where, for  $j \in \{0, 1\}$ ,  $k_j$  and  $l_j$  stand for the number of increments and decrements performed on  $r_j$ .

With a little abuse of notation, we use notation  $Q \rightarrow$  also for configurations of Minsky machines. We now state that the encoding is correct.

**Lemma 2 (Completeness).** Let  $(i, m_0, m_1)$  be a configuration of a Minsky machine N. Then, it holds:

1. If  $(i, m_0, m_1) \rightarrow then [(i, m_0, m_1)]_{M} \rightarrow$ 2. If  $(i, m_0, m_1) \longrightarrow_{M} (i', m'_0, m'_1) then, for some P, [(i, m_0, m_1)]_{M} \longrightarrow^* P \equiv [(i', m'_0, m'_1)]_{M}$ 

*Proof.* For (1) we have that if  $(i, m_0, m_1) \rightarrow$  then, by definition of Minsky machine, the program counter p is set to a non-existent instruction; i.e., for some  $i \notin [1..n]$ , p = i. Therefore, in process  $[(i, m_0, m_1)]_{M}$  no instruction is guarded by  $p_i$ . The thesis then follows by Proposition 3.

For (2) we proceed by a case analysis on the instruction performed by N. Hence, we distinguish three cases corresponding to the behaviors associated to rules M-JMP, M-DEC, and M-INC. Without loss of generality we assume instructions on register  $r_0$ .

**Case M-INC** We have a Minsky machine configuration  $(i, m_0^{k_0, l_0}, m_1^{k_1, l_1})$  with  $(i : INC(r_0))$ . Its encoding into  $Ho^{-f}$  is as follows:

$$\begin{split} [\![(i,m_0^{k_0,l_0},m_1^{k_1,l_1})]\!]_{\mathsf{M}} &= \overline{p_i} \parallel [\![r_0=m_0]\!]_{\mathsf{M}} \parallel [\![r_1=m_1]\!]_{\mathsf{M}} \parallel \prod_{h=1..n,i\neq h} [\![(h:I_h)]\!]_{\mathsf{M}} \parallel \\ & !p_i.\,(\overline{u_0} \parallel set_0(x).\,\overline{set_0}\langle x \parallel \operatorname{Inc}_0\rangle \parallel \overline{p_{i+1}}) \parallel \\ & loop.\,\operatorname{DIV} \parallel \overline{set_0}\langle \operatorname{Log}_0[k_0,l_0]\rangle \parallel \overline{set_1}\langle \operatorname{Log}_1[k_1,l_1]\rangle \end{split}$$

We begin by noting that the program counter  $p_i$  is consumed by the encoding of the instruction i. This leaves process  $\overline{u_0}$  unguarded; this represents the actual increment. We then have:

$$[\![(i,m_0^{k_0,l_0},m_1^{k_1,l_1})]\!]_{\mathsf{M}} \longrightarrow \equiv \overline{p_{i+1}} \parallel [\![r_0=m_0+1]\!]_{\mathsf{M}} \parallel set_0(x).\ \overline{set_0}\langle x \parallel \mathrm{Inc_0}\rangle \parallel S = T_1$$

where S stands for the rest of the system:

$$S = \llbracket r_1 = m_1 \rrbracket_{\mathsf{M}} \parallel \prod_{h=1}^n \llbracket (h:I_h) \rrbracket_{\mathsf{M}} \parallel loop. \, \mathsf{DIV} \parallel \overline{set_0} \langle \mathsf{LOG}_0[k_0,l_0] \rangle \parallel \overline{set_1} \langle \mathsf{LOG}_1[k_1,l_1] \rangle.$$

Now there is a synchronization on  $set_0$  for updating the log of register  $r_0$ :

$$\begin{split} T_1 & \longrightarrow \overline{p_{i+1}} \parallel [\![r_0 = m_0 + 1]\!]_{\mathsf{M}} \parallel [\![r_1 = m_1]\!]_{\mathsf{M}} \parallel \prod_{h=1}^n [\![(h:I_h)]\!]_{\mathsf{M}} \parallel \\ & loop. \, \mathsf{DIV} \parallel \overline{set_0} \langle \mathsf{LOG}_0[k_0 + 1, l_0] \rangle \parallel \overline{set_1} \langle \mathsf{LOG}_1[k_1, l_1] \rangle = T_2 \,. \end{split}$$

We notice that  $T_2 \equiv [(i+1,m_0+1^{k_0+1,l_0},m_1^{k_1,l_1})]_{\mathsf{M}}$ , as desired. **Case M-DEC** We have a Minsky machine configuration  $(i,m_0^{k_0,l_0},m_1^{k_1,l_1})$   $(r_0>0)$  with  $(i:\mathsf{DECJ}(r_0,s))$ . By definition, its encoding into  $\mathsf{Ho}^{-\mathsf{f}}$  is as follows:

$$\begin{split} \llbracket (i, m_0^{k_0, l_0}, m_1^{k_1, l_1}) \rrbracket_{\mathsf{M}} &= \overline{p_i} \parallel \llbracket r_0 = m_0 \rrbracket_{\mathsf{M}} \parallel \llbracket r_1 = m_1 \rrbracket_{\mathsf{M}} \parallel \prod_{h=1..n, i \neq h} \llbracket (h:I_h) \rrbracket_{\mathsf{M}} \parallel \\ & !p_i. \, \overline{m_i} \parallel !m_i. \, (\overline{loop} \parallel u_0. \, loop. \, set_0(x). \, \overline{set_0} \langle x \parallel \operatorname{DEC}_0 \rangle \parallel \overline{p_{i+1}}) \parallel \\ & !m_i. \, set_0(x). \, (x \parallel \overline{set_0} \langle \operatorname{Log}_0[k_0, l_0] \rangle \parallel \overline{p_s}) \parallel \\ & loop. \, \operatorname{DIV} \parallel \overline{set_0} \langle \operatorname{Log}_0[k_0, l_0] \rangle \parallel \overline{set_1} \langle \operatorname{Log}_1[k_1, l_1] \rangle \end{split}$$

The program counter is consumed by the encoding of the instruction i:

$$\begin{split} \llbracket (i, m_0^{k_0, l_0}, m_1^{k_1, l_1}) \rrbracket_{\mathsf{M}} &\longrightarrow \llbracket r_0 = m_0 \rrbracket_{\mathsf{M}} \parallel \overline{m_i} \parallel \\ & ! m_i. \left( \overline{loop} \parallel u_0. loop. set_0(x). \overline{set_0} \langle x \parallel \mathsf{DEC}_0 \rangle \parallel \overline{p_{i+1}} \right) \parallel \\ & ! m_i. set_0(x). \left( x \parallel \overline{set_0} \langle \mathbf{0} \rangle \parallel \overline{p_s} \right) \parallel S = T_1 \end{split}$$

where S stands for the rest of the system:

$$S = \llbracket r_1 = m_1 \rrbracket_{\mathsf{M}} \parallel \prod_{h=1}^n \llbracket (h:I_h) \rrbracket_{\mathsf{M}} \parallel loop. \, \mathsf{DIV} \parallel \overline{set_0} \langle \mathsf{LOG}_0[k_0,l_0] \rangle \parallel \overline{set_1} \langle \mathsf{LOG}_1[k_1,l_1] \rangle.$$

There is an internal choice on the name  $m_i$ . Let us suppose  $T_1$  makes the right guess, i.e., that process  $\overline{m_i}$  synchronizes with the first input-guarded process. We then have:

$$T_1 \longrightarrow \llbracket r_0 = m_0 \rrbracket_{\mathsf{M}} \parallel \overline{loop} \parallel u_0. \ loop. \ set_0(x). \ \overline{set_0} \langle x \parallel \mathsf{DEC}_0 \rangle \parallel \overline{p_{i+1}} \parallel S' = T_2.$$

where S' is the rest of the system, that now contains the input-guarded process that was not involved in the synchronization. Such a process is, however, stuck:

$$S' = S \parallel !m_i . set_0(x) . (x \parallel \overline{set_0} \langle \mathbf{0} \rangle \parallel \overline{p_s})$$

Since the content of  $r_0$  is greater than 0, a synchronization on  $u_0$  takes place, and the value of  $r_0$  decreases. Immediately after, there is also a synchronization on loop. More precisely, we have

$$T_2 \longrightarrow^2 \llbracket r_0 = m_0 - 1 \rrbracket_{\mathsf{M}} \parallel set_0(x). \overline{set_0} \langle x \parallel \mathsf{DEC}_0 \rangle \parallel \overline{p_{i+1}} \parallel S' = T_3.$$

Now the updating of the log associated to  $r_0$  can take place, and a synchronization on  $set_0$  is performed:

$$T_{3} \longrightarrow \overline{p_{i+1}} \parallel \llbracket r_{0} = m_{0} - 1 \rrbracket_{\mathsf{M}} \parallel \llbracket r_{1} = m_{1} \rrbracket_{\mathsf{M}} \parallel \prod_{h=1}^{n} \llbracket (h : I_{h}) \rrbracket_{\mathsf{M}} \parallel \\ loop. \, \mathsf{DIV} \parallel \overline{set_{0}} \langle \mathsf{LOG}_{0}[k_{0}, l_{0} + 1] \rangle \parallel \overline{set_{1}} \langle \mathsf{LOG}_{1}[k_{1}, l_{1}] \rangle = T_{4} \, .$$

Clearly,  $T_4 \equiv [(i+1, m_0 - 1^{k_0, l_0 + 1}, m_1^{k_1, l_1})]_{M}$ , as desired.

Case M-JMP This case is similar to the previous one. We have a Minsky machine configuration  $(i, m_0^{k_0, l_0}, m_1^{k_1, l_1})$  with  $(i : \text{DECJ}(r_0, s))$ . Since  $m_0 = 0$  we have that  $k_0 = l_0$ , and we proceed exactly as in the previous case. After synchronizing on  $p_i$  and spawning a new copy of (the encoding of) the instruction i, the process evolves to  $T_1$ . Once in  $T_1$  there is an internal choice on the name  $m_i$ . Again, let us suppose  $T_1$  makes the right guess, which in this case corresponds to the synchronization of  $\overline{m_i}$  and the second input-guarded process. We then have

$$T_1 \longrightarrow \llbracket r_0 = m_0 \rrbracket_{\mathsf{M}} \parallel set_0(x). (x \parallel \overline{set_0} \langle \mathbf{0} \rangle \parallel \overline{p_s}) \parallel S' = T_2.$$

where S' is the rest of the system, that is composed of S (as defined in the previous case) and of the input-guarded process on  $m_i$  that was not involved in the synchronization. Such a process is, however, stuck:

$$S' = S \parallel !m_i. (\overline{loop} \parallel u_0. loop. set_0(x). \overline{set_0} \langle x \parallel DEC_0 \rangle \parallel \overline{p_{i+1}})$$

Now there is a synchronization on  $set_0$ , and the log becomes unguarded:

$$\begin{split} T_2 &\longrightarrow \equiv \overline{p_s} \parallel \llbracket r_0 = m_0 \rrbracket_{\mathsf{M}} \parallel \llbracket r_1 = m_1 \rrbracket_{\mathsf{M}} \parallel \prod_{h=1}^n \llbracket (h:I_h) \rrbracket_{\mathsf{M}} \parallel \\ &loop. \, \mathsf{DIV} \parallel \prod_{k_0} \mathsf{INC}_0 \parallel \prod_{l_0} \mathsf{DEC}_0 \parallel \overline{set_0} \langle \mathbf{0} \rangle \parallel \overline{set_1} \langle \mathsf{Log}_1[k_1,l_1] \rangle = T_3 \,. \end{split}$$

Recall that  $k_0=l_0$ . Starting in  $T_3$ , we have that  $2\cdot k_0$  reductions take place: these are the interactions between an  ${\rm INC_0}$  and a corresponding  ${\rm DEC_0}$ . Half of these interactions correspond to synchronizations on  $check_0$ , whereas the rest are synchronizations on loop. All of these procesess are consumed. We then have that there exists a  $T_4$  such that (i)  $T_3 \longrightarrow^{2 \cdot k_0} T_4$  and (ii)  $T_4 \equiv [(s, m_0^{0,0}, m_1^{k_1, l_1})]_{\rm M}$ , as wanted.

**Lemma 3 (Soundness).** Let  $(i, m_0, m_1)$  be a configuration of a Minsky machine N. Given  $[\![(i, m_0, m_1)]\!]_{\mathsf{M}}$ , for some n > 0 and process  $P \in \mathsf{HO}^{-\mathsf{f}}$ , we have that:

```
    [(i, m<sub>0</sub>, m<sub>1</sub>)]<sub>M</sub> → <sup>n</sup> P and either:

            P ≡ [(i', m'<sub>0</sub>, m'<sub>1</sub>)]<sub>M</sub> and (i, m<sub>0</sub>, m<sub>1</sub>) →<sub>M</sub> (i', m'<sub>0</sub>, m'<sub>1</sub>), or
            P is a divergent process.

    For all 0 ≤ m < n, if [(i, m<sub>0</sub>, m<sub>1</sub>)]<sub>M</sub> → <sup>m</sup> P then, for some P', P → P'.
    If [(i, m<sub>0</sub>, m<sub>1</sub>)]<sub>M</sub> → then (i, m<sub>0</sub>, m<sub>1</sub>) →.
```

*Proof.* For (1), since n > 0, in all cases there is at least one reduction from  $[(i, m_0, m_1)]_{M}$ . An analysis of the structure of process  $[(i, m_0, m_1)]_{M}$  reveals that, in all cases, the first step corresponds to the consumption of the program counter  $p_i$ . This implies that there exists an instruction labeled with i, that can be executed from the configuration  $(i, m_0, m_1)$ . We proceed by a case analysis on the possible instruction, considering also the fact that the register on which the instruction acts can hold a value equal or greater than zero. We exploit the analysis reported for the proof of Lemma 2(2):

Case  $i: INC(r_0)$  Then the process evolves deterministically to  $P \equiv [(i+1, m_0 + 1, m_1)]_M$  in n=2 reductions.

Case  $i : DEC(r_0, s)$  with  $r_0 > 0$  Then the process evolves non-deterministically to one of the following cases:

- 1.  $P \equiv [(i+1, m_0-1, m_1)]_M$ , in n=5 reductions;
- 2. P = DIV, in n = 4 reductions. This is the case when the process makes a wrong guess on the content of the register (i.e. it assumes that  $r_0 = 0$ .)

Case  $i : DEC(r_0, s)$  with  $r_0 = 0$  Then the process evolves non-deterministically to one of the following cases (we use k and l to denote the number of increments and decrements on  $r_0$ , respectively):

- 1.  $P \equiv [(s, m_0, m_1)]_M$ , in n = 3 + k + l reductions;
- 2. P = DIV, in n = 3 + k + l + 1 reductions. This is the case when the process makes a wrong guess on the content of the register (i.e. it assumes that  $r_0 > 0$ .)

Notice that statement (2) follows easily from the above analysis.

As for (3), using Proposition 3 we know that if  $[(i, m_0, m_1)]_M \rightarrow$  then it is because  $p_i$  is not enabling any instruction. Hence,  $[(i, m_0, m_1)]_M$  corresponds to the encoding of a halting instruction and we have that  $(i, m_0, m_1) \rightarrow$ , as desired.

Summarizing Lemmata 2 and 3 we have the following:

**Theorem 1.** Let N be a Minsky machine with registers  $r_0 = m_0$ ,  $r_1 = m_1$ , instructions  $(1:I_1), \ldots, (n:I_n)$  and configuration  $(i,m_0,m_1)$ . Let P be the process  $[(i,m_0,m_1)]_M$  then  $(i,m_0,m_1)$  terminates if and only if P converges.

As a consequence of the results above we have that convergence is undecidable.

**Corollary 1.** Convergence is undecidable in  $Ho^{-f}$ .

#### 4 Termination is Decidable

In this section we prove that termination is decidable for HO<sup>-f</sup> processes. As hinted at in the introduction, this is in sharp contrast with the analogous result for HOCORE. The proof appeals to the theory of well-structured transition systems, whose main definitions and results we summarize next.

**Well-Structured Transition Systems.** The following results and definitions are from [8], unless differently specified. Recall that a *quasi-order* (or, equivalently, preorder) is a reflexive and transitive relation.

**Definition 7 (Well-quasi-order).** A well-quasi-order (wqo) is a quasi-order  $\leq$  over a set X such that, for any infinite sequence  $x_0, x_1, x_2 \ldots \in X$ , there exist indexes i < j such that  $x_i \leq x_j$ .

Note that if  $\leq$  is a wqo then any infinite sequence  $x_0, x_1, x_2, \ldots$  contains an infinite increasing subsequence  $x_{i_0}, x_{i_1}, x_{i_2}, \ldots$  (with  $i_0 < i_1 < i_2 < \ldots$ ). Thus well-quasi-orders exclude the possibility of having infinite strictly decreasing sequences.

We also need a definition for (finitely branching) transition systems. This can be given as follows. Here and in the following  $\rightarrow^*$  denotes the reflexive and transitive closure of the relation  $\rightarrow$ .

**Definition 8 (Transition system).** A transition system is a structure  $TS = (S, \rightarrow)$ , where S is a set of states and  $\rightarrow \subseteq S \times S$  is a set of transitions. We define Succ(s) as the set  $\{s' \in S \mid s \rightarrow s'\}$  of immediate successors of S. We say that TS is finitely branching if, for each  $s \in S$ , Succ(s) is finite.

**Fact 1** The LTS for HO<sup>-f</sup> given in Section 2 is finitely branching.

The function Succ will be used also on sets by assuming that in this case they are defined by the point-wise extension of the above definitions.

The key tool to decide several properties of computations is the notion of well-structured transition system. This is a transition system equipped with a well-quasi-order on states which is (upward) compatible with the transition relation. Here we will use a strong version of compatibility, hence the following definition.

**Definition 9** (Well-structured transition system). A well-structured transition system with strong compatibility is a transition system  $TS = (S, \rightarrow)$ , equipped with a quasi-order < on S, such that the two following conditions hold:

- 1.  $\leq$  is a well-quasi-order;
- 2.  $\leq$  is strongly (upward) compatible with  $\rightarrow$ , that is, for all  $s_1 \leq t_1$  and all transitions  $s_1 \rightarrow s_2$ , there exists a state  $t_2$  such that  $t_1 \rightarrow t_2$  and  $s_2 \leq t_2$  holds.

The following theorem is a special case of Theorem 4.6 in [8] and will be used to obtain our decidability result.

**Theorem 2.** Let  $TS = (S, \rightarrow, \leq)$  be a finitely branching, well-structured transition system with strong compatibility, decidable  $\leq$  and computable Succ. Then the existence of an infinite computation starting from a state  $s \in S$  is decidable.

We will also need a result due to Higman [10] which allows to extend a well-quasiorder from a set S to the set of the finite sequences on S. To be more precise, given a set S let use denote by  $S^*$  the set of finite sequences built by using elements in S. We can define a quasi-order on  $S^*$  as follows. **Definition 10.** Let S be a set and  $\leq a$  quasi-order over S. The relation  $\leq_*$  over  $S^*$  is defined as follows. Let  $t, u \in S^*$ , with  $t = t_1 t_2 \dots t_m$  and  $u = u_1 u_2 \dots u_n$ . We have that  $t \leq_* u$  iff there exists an injection f from  $\{1, 2, \dots m\}$  to  $\{1, 2, \dots n\}$  such that  $t_i \leq u_{f(i)}$  and  $i \leq f(i)$  for  $i = 1, \dots, m$ .

The relation  $\leq_*$  is clearly a quasi-order over  $S^*$ . It is also a wqo, since we have the following result.

**Lemma 4** (Higman [10]). Let S be a set and  $\leq a$  wqo over S. Then  $\leq_*$  is a wqo over  $S^*$ .

Finally we will use also the following proposition, whose proof is immediate.

**Proposition 4.** Let S be a finite set. Then the equality is a wqo over S.

**Termination is Decidable in Ho^{-f}.** Here we prove that termination is decidable in  $Ho^{-f}$ . The crux of the proof consists in finding an upper bound for a process and its derivatives. This is possible in  $Ho^{-f}$  because of the limited structure allowed in output actions.

We begin by defining a notion of normal form for Ho<sup>-f</sup> processes. We then characterize an upper bound for the derivatives of a given process, and we define an ordering over them. This ordering is then shown to be a wqo that is strongly compatible with respect to the LTS of Ho<sup>-f</sup> given in Section 2. The decidability result is then obtained by resorting to the results from [8] reported before.

**Definition 11 (Normal Form).** Let  $P \in \operatorname{Ho}^{-f}$ . P is in normal form iff

$$P = \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). P_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle P_j' \rangle$$

where each  $P_i$  and  $P'_i$  are in normal form.

**Lemma 5.** Every process  $P \in Ho^{-f}$  is structurally congruent to a normal form.

*Proof.* By induction on the structure of P. The base cases are when  $P=\mathbf{0}$  and when P=x, and are immediate. Cases  $P=\overline{a}\langle Q\rangle$  and P=a(x). Q follow by applying the inductive hypothesis on Q. For the case  $P=P_1\parallel P_2$ , we apply the inductive hypothesis twice and we obtain that

$$P_1 \equiv \prod_{k=1}^l x_k \parallel \prod_{i=1}^m a_i(y_i). \ P_i \parallel \prod_{j=1}^n \overline{b_j} \langle P_j \rangle \ \ \text{and} \ \ P_2 \equiv \prod_{k=1}^{l'} x_k \parallel \prod_{i=1}^{m'} a_i'(y_i'). \ P_i' \parallel \prod_{j=1}^{n'} \overline{b_j'} \langle P_j' \rangle \ .$$

It is then easy to see that  $P_1 \parallel P_2$  is structurally congruent to a normal form, as desired.

We now define an ordering over normal forms. Intuitively, a process is larger than another if it has more parallel components.

**Definition 12 (Relation**  $\preceq$ ). Let  $P, Q \in \operatorname{Ho}^{-f}$ . We write  $P \preceq Q$  iff there exist  $x_1 \ldots x_l$ ,  $P_1 \ldots P_m, P'_1 \ldots P'_n, Q_1 \ldots Q_m, Q'_1 \ldots Q'_n$ , and R such that

$$P \equiv \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). P_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle P_j' \rangle$$
  
$$Q \equiv \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). Q_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle Q_j' \rangle \parallel R$$

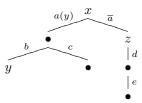
with  $P_i \leq Q_i$  and  $P'_i \leq Q'_i$ , for  $i \in [1..m]$  and  $j \in [1..n]$ .

The normal form of a process can be intuitively represented in a tree-like manner. More precisely, given the process in normal form

$$P = \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). P_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle P_j' \rangle$$

we shall decree its associated tree to have a root node labeled  $x_1, \ldots, x_k$ . This root node has m+n children, corresponding to the trees associated to processes  $P_1, \ldots, P_m$  and  $P'_1, \ldots, P'_m$ ; the outgoing edges connecting the root node and the children are labeled  $a_1(y_1), \ldots, a_m(y_m)$  and  $\overline{b_1}, \ldots, \overline{b_n}$ .

*Example 1.* Process  $P=x\parallel a(y)$ .  $(b.y\parallel c)\parallel \overline{a}\langle z\parallel d.e\rangle$  has the following tree representation:



The intuition given before on the tree representation of a process in normal form will be useful now to reason about the structure of HO<sup>-f</sup> terms. We begin by defining the *depth* of a process. Notice that the depth a process corresponds to the maximum depth of its tree representation.

**Definition 13** (**Depth).** Let  $P = \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i)$ .  $P_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle P'_j \rangle$  be a HO<sup>-f</sup> process in normal form. The depth of P is given by

$$depth(P) = max\{1 + depth(P_i), 1 + depth(P_i') \mid i \in [1..m] \land j \in [1..n]\}.$$

Given a natural number n and a process P, the set  $\mathcal{P}_{P,n}$  contains all those processes in normal form that (i) can be built using the alphabet of P and (ii) whose depth is at most n

**Definition 14.** Let n be a natural number and  $P \in Ho^{-f}$ . We define the set  $\mathcal{P}_{P,n}$  as follows:

$$\mathcal{P}_{P,n} = \{ Q \mid Q \equiv \prod_{k \in K} x_k \parallel \prod_{i \in I} a_i(y_i) \cdot Q_i \parallel \prod_{j \in J} \overline{b_j} \langle Q_j' \rangle \\ \wedge \mathcal{A}(Q) \subseteq \mathcal{A}(P) \\ \wedge Q_i, Q_j' \in \mathcal{P}_{P,n-1} \ \forall i \in I, j \in J \}$$

where  $\mathcal{P}_{P,0}$  contains processes that are built out only of variables in  $\mathcal{A}(P)$ .

As it will be shown later, the set of all derivatives of P is a subset of  $\mathcal{P}_{P,2\text{-depth}(P)}$ . When compared to processes in languages such as Milner's CCS, higher order processes have a more complex structure. This is because, by virtue of reductions, an arbitrary process can take the place of possibly several occurrences of a single variable. As a consequence, the depth of (the syntax tree of) a process cannot be determined (or even approximated) before its execution: it can vary arbitrarily along reductions. Crucially, in  $\mathrm{Ho}^{-\mathrm{f}}$  it is possible to bound such a depth. Our approach is the following: rather than solely depending on the depth of a process, we define measures on the relative position of variables within a process. Informally speaking, such a position will be determined by the number of prefixes that guard a variable. Notice that since variables are allowed only at the top level of the output objects, the distance of such variables remains invariant during reductions. This allows to obtain a bound on the structure for  $\mathrm{Ho}^{-\mathrm{f}}$  processes. Finally, it is worth stressing that even if the same notions of normal form, depth, and distance can be defined for  $\mathrm{Ho}^{-\mathrm{f}}$  finite upper bound for such a language does not exist.

We now define the maximum distance between a variable and its binder.

**Definition 15.** Let  $P = \prod_{k \in K} x_k \parallel \prod_{i \in I} a_i(y_i)$ .  $P_i \parallel \prod_{j \in J} \overline{b_j} \langle P'_j \rangle$  be a Ho<sup>-f</sup> process in normal form. We define the maximum distance of P as:

$$\begin{aligned} \mathsf{maxDistance}(P) &= \max\{\mathsf{maxDist}_{y_i}(P_i), \\ &\quad \mathsf{maxDistance}(P_i), \mathsf{maxDistance}(P_i') \mid i \in I, j \in J\} \end{aligned}$$

where

$$\mathsf{maxDist}_x(P) = \begin{cases} 1 & \text{ if } P = x, \\ 1 + \mathsf{maxDist}_x(P_z) & \text{ if } P = a(z).\, P_z \wedge \, x \neq z, \\ 1 + \mathsf{maxDist}_x(P') & \text{ if } P = \overline{a}\langle P' \rangle, \\ \mathsf{max}\{\mathsf{maxDist}_x(R), \mathsf{maxDist}_x(Q)\} & \text{ if } P = R \parallel Q, \\ 0 & \text{ otherwise}. \end{cases}$$

**Lemma 6** (**Properties of maxDistance**). Let P be a HO<sup>-f</sup> process. It holds that:

- 1.  $maxDistance(P) \le depth(P)$
- 2. For every Q such that  $P \xrightarrow{\alpha} Q$ , maxDistance $(Q) \leq \max D$ istance(P).

*Proof.* Part (1) is immediate from Definitions 13 and 15. Part (2) follows by a case analysis on the rule used to infer  $\stackrel{\alpha}{\longrightarrow}$ . We focus in the case TAU1: the other cases are similar or simpler. We then have that  $P \equiv \overline{a} \langle S \rangle \parallel a(x)$ .  $R \parallel T$  and  $Q \equiv R\{S/x\} \parallel T$ . Applying Definition 15 in both processes, we obtain

$$\begin{split} \max \mathsf{Distance}(P) &= \max \{ \mathsf{maxDistance}(S), \mathsf{maxDist}_x(R), \\ & \mathsf{maxDistance}(R), \mathsf{maxDistance}(T) \} \\ \max \mathsf{Distance}(Q) &= \max \{ \mathsf{maxDistance}(R\{S\!/\!x\}), \mathsf{maxDistance}(T) \} \,. \end{split}$$

We can thus disregard the contribution of  $\max Distance(T)$ , since it does not participate in the synchronization. We then focus on determining  $\max Distance(R\{S/x\})$ . We begin

by recalling that by the syntax of  $\operatorname{Ho}^{-f}$ ,  $S \equiv x_1 \parallel \cdots \parallel x_k \parallel S'$ , where S' is a closed process. The free variables in S can affect  $\operatorname{maxDistance}(R\{S/x\})$  in essentially two ways:

1. The free variables of S do not get captured by a binder in R. In this case, they do not contribute to maxDistance( $R\{S/x\}$ ), and we have that

$$\max Distance(R\{S/x\}) = \max \{\max Distance(R), \max Distance(S)\}$$

and the thesis holds.

2. Some of the free variables of S get captured by a binder in R. In this case we find it convenient to appeal to the tree representations of R and S, denoted  $T_R$  and  $T_S$ , respectively. Notice that  $T_S$  is a tree where the root node is labeled with  $x_1, \ldots, x_k$  and whose only descendant is  $T_S'$ , the tree representation of S'. Consider now  $T_Q$ , the tree representation of  $R\{S/x\}$ : it corresponds to the tree  $T_R$  in which all occurrences of x in the nodes of  $T_R$  have been replaced with  $T_S$ . Crucially, the height of  $x_1, \ldots, x_k$  is exactly the same height of x, so the distance with respect their binder does not increase. Also, since S' does not contain free variables, the contribution of maxDistance(S') to P remains invariant in Q. We then conclude that the thesis holds also in this case.

We now define the maximum depth of processes that can be communicated. Notice that the continuations of inputs are considered as they could become communication objects themselves along reductions:

**Definition 16.** Let  $P = \prod_{k \in K} x_k \parallel \prod_{i \in I} a_i(y_i)$ .  $P_i \parallel \prod_{j \in J} \overline{b_j} \langle P_j' \rangle$  be a HO<sup>-f</sup> process in normal form. We define the maximum depth of a process that can be communicated (maxDepCom(P)) in P as:

```
\mathsf{maxDepCom}(P) = \max\{\mathsf{maxDepCom}(P_i), \mathsf{depth}(P_j') \mid i \in I, j \in J\} \;.
```

**Lemma 7** (**Properties of maxDepCom**). Let P be a HO<sup>-f</sup> process. It holds that:

- 1.  $maxDepCom(P) \le depth(P)$
- 2. For every Q such that  $P \xrightarrow{\alpha} Q$ , maxDepCom(Q) < maxDepCom(P).

*Proof.* Part (1) is immediate from Definitions 13 and 16. Part (2) follows by a case analysis on the rule used to infer  $\stackrel{\alpha}{\longrightarrow}$ . We focus in the case TAU1: the other cases are similar or simpler. We then have that  $P \equiv \overline{a} \langle S \rangle \parallel a(x)$ .  $R \parallel T$  and  $Q \equiv R\{S/x\} \parallel T$ . Applying Definition 15 in both processes, we obtain

```
\begin{split} \max & \mathsf{DepCom}(P) = \max \{ \mathsf{maxDepCom}(T), \mathsf{maxDepCom}(S), \mathsf{depth}(S) \} \\ \max & \mathsf{DepCom}(Q) = \max \{ \mathsf{maxDepCom}(T), \mathsf{maxDepCom}(R\{S/x\}) \} \,. \end{split}
```

We now focus on analyzing the influence a substitution has on communicated objects. More precisely, since variables can occur in output objects, we analyze whether x occurs free in some communication object in R. We thus have two cases:

- 1. There are no output objects with free occurrences of x. Then S will only occur at the top level in  $R\{S/x\}$ . Since  $\operatorname{depth}(S)$  was already taken into account when  $\operatorname{determining\ maxDepCom}(P)$ , we then have that  $\operatorname{maxDepCom}(R\{S/x\}) \leq \operatorname{maxDepCom}(P)$ , and the thesis holds.
- 2. Some output objects have free occurrences of x. Then, there exists a process  $P_x = x \parallel x_1 \parallel \cdots \parallel x_k \parallel S'$  so that an output action  $\bar{b}\langle P_x \rangle$  occurs in R. Recall that by definition of  $\mathrm{Ho^{-f}}$ , S' is a closed process. Therefore, the process  $\bar{b}\langle P_x\{S/x\}\rangle$  occurs in  $R\{S/x\}$ . Clearly, an eventual increase of  $\mathrm{maxDepCom}(Q)$  depends on the depth of  $P_x\{S/x\}$ . We have that  $\mathrm{depth}(P_x\{S/x\}) = \mathrm{max}(\mathrm{depth}(S), \mathrm{depth}(S'))$ . Since both  $\mathrm{depth}(S)$  and  $\mathrm{depth}(S')$  were considered when determining  $\mathrm{maxDepCom}(P)$ , we conclude that  $\mathrm{maxDepCom}(R\{S/x\})$  can be at most equal to  $\mathrm{maxDepCom}(P)$ , and so the thesis holds.

**Notation 2** We use  $P \xrightarrow{\widetilde{\alpha}} P'$  if, for some  $n \geq 0$ , there exist  $\alpha_1, \ldots, \alpha_n$  such that  $P \xrightarrow{\alpha_1} \cdots \xrightarrow{\alpha_n} P'$ .

Generalizing Lemmata 6 and 7 we obtain:

**Corollary 2.** Let P be a  $Ho^{-f}$  process. For every Q such that  $P \xrightarrow{\tilde{\alpha}} Q$ , it holds that:

- 1.  $maxDistance(Q) \le depth(P)$
- 2.  $maxDepCom(Q) \leq depth(P)$ .

We are interested in characterizing the derivatives of a given process P. We shall show that they are over-approximated by means of the set  $\mathcal{P}_{P,2\cdot \mathsf{depth}(P)}$ . Over such an approximation we will investigate the properties of the relation  $\preceq$ ; such properties will then hold also for the set of derivatives.

**Definition 17.** Let 
$$P \in \operatorname{Ho}^{-f}$$
. Then we define  $\operatorname{Deriv}(P) = \{Q \mid P \longrightarrow^* Q\}$ 

The following results hold because of the limitations we have imposed on the output actions for  $\mathrm{Ho}^{-f}$  processes.

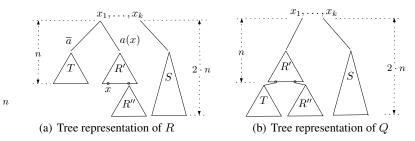
**Lemma 8.** Let P be an  $Ho^{-f}$  process.  $P \in \mathcal{P}_{P,n}$  if and only if  $depth(P) \leq n$ .

*Proof.* The "if" direction is straightforward by definition of  $\mathcal{P}_{P,n}$  (Definition 14). For the "only if" direction we proceed by induction on n. If n=0 then  $P=\mathbf{0}$  or  $P=x_1\parallel\cdots\parallel x_k$ . In both cases, P is easily seen to be in  $\mathcal{P}_{P,0}$ . If n>0 then

$$P = \prod_{k \in K} x_k \parallel \prod_{i \in I} a_i(y_i). P_i \parallel \prod_{j \in J} \overline{b_j} \langle P_j' \rangle$$

where, for every  $i \in I$  and  $j \in J$ , both  $\operatorname{depth}(P_i) \leq \operatorname{depth}(P) \leq n-1$  and  $\operatorname{depth}(P'_j) \leq \operatorname{depth}(P) \leq n-1$ . By inductive hypothesis, each  $P_i$  and  $P'_j$  is in  $\mathcal{P}_{P,n-1}$ . Then, by Definition 14,  $P \in \mathcal{P}_{P,n}$  and we are done.

**Proposition 5.** Let P be a  $\operatorname{HO}^{-f}$  process. Suppose, for some n, that  $P \in \mathcal{P}_{P,n}$ . For every Q such that  $P \xrightarrow{\alpha} Q$ , it holds that  $Q \in \mathcal{P}_{P,2\cdot n}$ .



**Fig. 1.** Tree representation of a reduction  $R \longrightarrow Q$ , as in the proof of Lemma 9.

*Proof.* We proceed by case analysis on the rule used to infer  $\stackrel{\alpha}{\longrightarrow}$ . We focus on the case such a rule is TAU1; the remaining cases are similar or simpler. Recall that by Lemmata 6(1) and 7(1) the maximum distance between an occurrence of a variable and its binder is bounded by depth(P). By Definition 14 any process that can be communicated in P is in  $\mathcal{P}_{P,n-1}$  and its maximum depth is also bounded by depth(P) —which, in turn, by Lemma 8, is bounded by n. The deepest position for a variable is when it is a leaf in the tree associated to the normal form of P. That is, when its depth is exactly depth(P). If in that position we place a process in  $\mathcal{P}_{P,n-1}$  — whose depth is also depth(P) — then it is easy to see that (the associated tree of) Q has a depth of  $2 \cdot \text{depth}(P)$ , which is bounded by  $2 \cdot n$ . Hence, by Lemma 8, Q is in  $\mathcal{P}_{P,2\cdot n}$ .

The lemma below generalizes Proposition 5 to a sequence of reductions.

**Lemma 9.** Let P be a  $\operatorname{Ho}^{-f}$  process. Suppose, for some n, that  $P \in \mathcal{P}_{P,n}$ . For every Q such that  $P \stackrel{\widetilde{\alpha}}{\longrightarrow} Q$ , it holds that  $Q \in \mathcal{P}_{P,2\cdot n}$ .

*Proof.* The proof proceeds by induction on k, the number of reductions, exploiting Proposition 5. The base case is when k=1, and it follows by Proposition 5. For the inductive step we assume k>1, so we have that  $P\stackrel{\alpha_1}{\longrightarrow}\cdots\stackrel{\alpha_{k-1}}{\longrightarrow}R\stackrel{\alpha_k}{\longrightarrow}Q$ . By induction hypothesis we know that  $R\in\mathcal{P}_{P,2\cdot n}$ . We then proceed by a case analysis on the rule used to infer  $R\stackrel{\alpha_k}{\longrightarrow}Q$ . We content ourselves with illustrating the case TAU1; the other ones are similar or simpler. We then have that  $R\equiv x_1\parallel\cdots\parallel x_k\parallel \overline{a}\langle T\rangle\parallel a(x).$   $R'\parallel S$  and that  $Q\equiv x_1\parallel\cdots\parallel x_k\parallel R'\{T/x\}\parallel S$ .

The tree representation of process R is depicted in Figure 1 (a). There, R'' is used to represent a subprocess of R'. By Corollary 2 the maximum distance between x and its binder a(x) is  $\operatorname{depth}(P)$ , which in turn is bounded by n (Lemma 8). Moreover, the maximum depth of T is bounded by  $\operatorname{maxDepCom}(P)$ ; by Corollary 2,  $\operatorname{depth}(P) \leq n$ . The tree representation of Q is given in Figure 1 (b). We then conclude that because of the limitations on the structure of the communicated objects the overall depth of process Q is  $2 \cdot \operatorname{depth}(P)$ . Hence, and by using Lemma 8,  $Q \in \mathcal{P}_{P,2\cdot n}$ , as wanted.  $\square$ 

**Corollary 3.** Let  $P \in \operatorname{Ho}^{-f}$ . Then  $\operatorname{Deriv}(P) \subseteq \mathcal{P}_{P,2 \cdot \operatorname{depth}(P)}$ .

We are now ready to prove that relation  $\leq$  is a wqo. We begin by showing that it is a quasi-order.

### **Proposition 6.** The relation $\leq$ is a quasi-order.

*Proof.* We need to show that  $\leq$  is both reflexive and transitive. From Definition 11, reflexivity is immediate.

Transitivity implies proving that, given processes P,Q, and R such that  $P \leq Q$  and  $Q \leq R$ ,  $P \leq R$  holds. We proceed by induction on  $k = \operatorname{depth}(P)$ . If k = 0 then we have that  $P = x_1 \parallel \cdots \parallel x_k$ . Since  $P \leq Q$ , we have that  $Q = x_1 \parallel \cdots \parallel x_k \parallel S$ . and that  $R = x_1 \parallel \cdots \parallel x_k \parallel S'$ , for some S,S' such that  $S \leq S'$ . By Definition 12, the thesis follows. Now suppose k > 0. By Definition 11 and by hypothesis we have the following:

$$P = \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). P_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle P'_j \rangle$$

$$Q = \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). Q_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle Q'_j \rangle \parallel S$$

$$R = \prod_{k=1}^{l} x_k \parallel \prod_{i=1}^{m} a_i(y_i). R_i \parallel \prod_{j=1}^{n} \overline{b_j} \langle R'_j \rangle \parallel S \parallel T.$$

with  $P_i \leq Q_i$ ,  $P'_j \leq Q'_j$ ,  $Q_i \leq R_i$ , and  $Q'_j \leq R'_j$   $(i \in I, j \in J)$ . Since  $P_i$ ,  $P'_j$ ,  $Q_i$ ,  $Q'_j$ ,  $R_i$ , and  $R'_j$  have depth k-1, by inductive hypothesis  $P_i \leq R_i$  and  $P'_j \leq R'_j$ . By Definition 12, the thesis follows and we are done.

We are now in place to state that  $\leq$  is a wqo.

**Theorem 3 (Well-quasi-order).** Let  $P \in \operatorname{Ho}^{-f}$  and  $n \geq 0$ . The relation  $\leq$  is a well-quasi-order over  $\mathcal{P}_{P,n}$ .

*Proof.* The proof is by induction on n.

- Let n=0. Then  $\mathcal{P}_{P,0}$  contains processes containing only variables taken from  $\mathcal{A}(P)$ . The equality on finite sets is a well-quasi-ordering; by Lemma 4 (Higman's Lemma) also  $=_*$  is a well quasi-ordering: it corresponds to the ordering  $\leq$  on processes containing only variables.
- Let n>0. Take an infinite sequence of processes  $s=P_1,P_2,\ldots,P_l,\ldots$  with  $P_l\in\mathcal{P}_{P,n}$ . We shall show that the thesis holds by means of successive filterings of the normal forms of the processes in s. By Lemma 5 there exist  $K_l$ ,  $I_l$  and  $J_l$  such that

$$P_l \equiv \prod_{k \in K_l} x_k \parallel \prod_{i \in I_l} a_i(y_i). P_i^l \parallel \prod_{j \in J_l} \overline{b_j} \langle P_j'^l \rangle$$

with  $P_i^l$  and  $P_j^{\prime l} \in \mathcal{P}_{P,n-1}$ . Hence each  $P_l$  can be seen as composed of 3 finite sequences: (i)  $x_1 \dots x_k$ , (ii)  $a_1(y_1).P_1^l \dots a_i(y_i).P_i^l$ , and (iii)  $\overline{b_1}\langle P_1^{\prime l}\rangle \dots \overline{b_j}\langle P_j^{\prime l}\rangle$ . We note that the first sequence is composed of variables from the finite set  $\mathcal{A}(P)$  whereas the other two sequences are composed by elements in  $\mathcal{A}(P)$  and  $\mathcal{P}_{P,n-1}$ . Since we have an infinite sequence of  $\mathcal{A}(P)^*$ , as  $\mathcal{A}(P)$  is finite, by Proposition 4 and Lemma 4 we have that  $=_*$  is a wqo over  $\mathcal{A}(P)^*$ .

By inductive hypothesis, we have that  $\preceq$  is a wqo on  $\mathcal{P}_{P,n-1}$ , hence by Lemma 4 relation  $\preceq_*$  is a wqo on  $\mathcal{P}_{P,n-1}^*$ . We start filtering out s by making the finite sequences  $x_1 \ldots x_k$  increasing with respect to  $=_*$ ; let us call this subsequence t. Then we filter out t, by making the finite sequence  $a_1(y_1).P_1^l\ldots a_i(y_i).P_i^l$  increasing with respect to both  $\preceq_*$  and  $=_*$ . This is done in two steps: first, by considering the relation  $=_*$  on the subject of the actions (recalling that  $a_i,y_i\in\mathcal{A}(P)$ ), and then by applying another filtering to the continuation using the inductive hypothesis. For the first step, it is worth remarking that we do not consider symbols of the alphabet but pairs of symbols. Since the set of pairs on a finite set is still finite, we know by Higman's Lemma that  $=_*$  is a wqo on the set of sequences of pairs  $(a_i, y_i)$ .

For the sequence of outputs  $\overline{b_1}\langle P_1'^l\rangle\dots\overline{b_j}\langle P_j'^l\rangle$  this is also done in two steps: the subject of the outputs are ordered with respect to  $=_*$  and the objects of the output action are ordered with respect to  $\leq_*$  using the inductive hypothesis.

At the end of the process we obtain an infinite subsequence of s that is ordered with respect to  $\leq$ .

The last thing to show is that the well-quasi-ordering  $\leq$  is strongly compatible with respect to the (finitely branching) LTS associated to  $\mathrm{Ho}^{-f}$ . We need the following auxiliary lemma:

**Lemma 10.** Let P, P', Q, and Q' be  $\operatorname{HO}^{-f}$  processes in normal form such that  $P \leq P'$  and  $Q \leq Q'$ . It holds that  $P\{Q/x\} \leq P'\{Q'/x\}$ .

*Proof.* By induction on the structure of P.

- 1. Cases  $P = \mathbf{0}$  and P = y, for some  $y \neq x$ : Immediate.
- 2. Case P=x. Then  $P'=x\parallel N$ , for some process N. We have that  $P\{Q/x\}=Q$  and that  $P'\{Q'/x\}=Q'\parallel N\{Q'/x\}$ . Since  $Q\preceq Q'$  the thesis follows.
- 3. Case P=a(y). R. Then P'=a(y).  $R' \parallel N$ , for some process N. Since by hypothesis  $P \leq P'$ , then  $R \leq R'$ . We then have that  $P\{Q/x\} = a(y)$ .  $R\{Q/x\}$  and that  $P'\{Q'/x\} = a(y)$ .  $R'\{Q'/x\}$   $M \parallel N\{Q'/x\}$ . By inductive hypothesis we obtain that  $R\{Q/x\} \leq R'\{Q'/x\}$ , and the thesis follows.
- 4. Case  $P = \overline{a}\langle R \rangle$ : Similar to (3).
- 5. Case  $P=R\parallel S$ . Then  $P'=R'\parallel S'\parallel N$ , for some process N, with  $R\preceq R'$  and  $S\preceq S'$ . We then have that  $P\{Q/x\}=R\{Q/x\}\parallel S\{Q/x\}$  and  $P'\{Q'/x\}=R'\{Q'/x\}\parallel S'\{Q'/x\}\parallel N\{Q'/x\}$ . The thesis then follows by inductive hypothesis.

**Theorem 4 (Strong Compatibility).** Let  $P, Q, P' \in \operatorname{Ho}^{-f}$ . If  $P \preceq Q$  and  $P \xrightarrow{\alpha} P'$  then there exists Q' such that  $Q \xrightarrow{\alpha} Q'$  and  $P' \preceq Q'$ .

*Proof.* By case analysis on the rule used to infer transition  $\stackrel{\alpha}{\longrightarrow}$ . We content ourselves with illustrating the cases INP and TAU1; the other ones are similar or simpler.

**Case INP** Then P = a(y).  $P_1$  and Q = a(y).  $Q_1$ , with  $P_1 \leq Q_1$ . If  $P \xrightarrow{a(y)} P' \equiv P_1$  then also  $Q \xrightarrow{a(y)} Q' \equiv Q_1$ . We then have that  $P' \leq Q'$ , as desired.

Case TAU1 Then  $P = \overline{a}\langle P_1 \rangle \parallel a(y)$ .  $P_2 \parallel N$  and  $Q = \overline{a}\langle Q_1 \rangle \parallel a(y)$ .  $Q_2 \parallel N'$ , with  $P_1 \preceq P_2$ ,  $Q_1 \preceq Q_2$ , and  $N \preceq N'$ . If  $P \xrightarrow{\tau} P' \equiv P_2\{P_1/y\} \parallel N$  then also  $Q \xrightarrow{\tau} Q' \equiv Q_2\{Q_1/y\} \parallel N'$ . By Lemma 10 we have  $P_2\{P_1/y\} \preceq Q_2\{Q_1/y\}$ ; using this and the hypothesis the thesis follows.

**Theorem 5.** Let  $P \in \operatorname{Ho}^{-f}$ . The transition system  $(\operatorname{Deriv}(P), \longrightarrow, \preceq)$  is a finitely branching well-structured transition system with strong compatibility, decidable  $\preceq$ , and computable Succ.

*Proof.* The transition system of  $Ho^{-f}$  is finitely branching (Fact 1). The fact that  $\leq$  is a well-quasi-order on Deriv(P) follows from Corollary 3 and Theorem 3. Strong compatibility follows from Theorem 4.

We can now state the main technical result of the section:

**Corollary 4.** Let  $P \in Ho^{-f}$ . Termination of P is decidable.

*Proof.* This follows from Theorem 2 and Theorem 5.

# 5 Concluding Remarks

We have studied Ho<sup>-f</sup>, a higher-order process calculi featuring a limited form of higher-order communication. In Ho<sup>-f</sup>, output actions can only include previously received processes in composition with closed ones. This is reminiscent of programming abstractions/paradigms with forms of code mobility in which the recipient is not authorized or capable to access/modify the structure of the received code. The limitation in output actions represents a weakening of the forward capabilities of higher-order processes. Here we have shown it also has consequences both on the expressiveness of the language and on the decidability of termination.

As for the expressiveness issues, by exhibiting an encoding of Minsky machines into Ho<sup>-f</sup>, we have shown that convergence is undecidable. Hence, from an absolute expressiveness standpoint, Ho<sup>-f</sup> is Turing complete. Now, given the analogous result for HOCORE [4], a relative expressiveness issue also arises. Indeed, our encoding of Minsky machines into Ho<sup>-f</sup> is not faithful, which reveals a difference on the criteria each encoding satisfies. This reminds us of the situation in [11], for encodings of Turing complete formalisms into calculi with interruption and compensation. That work offers a detailed comparison of the criteria faithful and unfaithful encodings satisfy. For the sake of space, we do not elaborate further on their exact definition; using the terminology in [11], it suffices to say that the presented encoding satisfies a weakly Turing completeness criterion, as opposed to the (stronger) Turing completeness criterion that is satisfied by the encoding of Minsky machines into HOCORE in [4]. The discrepancy on the criteria satisfied by each encoding might be interpreted as an expressiveness gap between HO<sup>-f</sup> and HOCORE; nevertheless, it seems clear that the loss of expressiveness resulting from limiting the forwarding capabilities in HOCORE is much less dramatic than what one would have expected.

The communication style of HO<sup>-f</sup> causes a separation result with respect to HOCORE. In fact, because of the limitation on output actions, it was possible to prove that termination in HO<sup>-f</sup> is decidable. This is in sharp contrast with the situation in HOCORE, where termination is undecidable. In HO<sup>-f</sup>, it is possible to provide an upper bound on the depth (i.e. the level of nesting of actions) of the (set of) derivatives of a process. In HOCORE such an upper bound does not exist. This was essential for obtaining the decidability result; for this, we appealed to the approach developed in [9], which relies on the theory of well-structured transition systems [8]. As far as we are aware, this approach for expressiveness has not previously been used in the higher-order setting. The decidability of termination is significant, as it might shed light on the development of verification techniques for higher-order processes.

The HO<sup>-f</sup> calculus is a sublanguage of HOCORE. As such, HO<sup>-f</sup> inherits the many results and properties of HOCORE [4]; most notably, a notion of (strong) bisimilarity which is decidable and coincides with a number of sensible equivalences in the higher-order context. Our results thus complement those in [4] and deepen our understanding of the expressiveness of core higher-order calculi as a whole. Furthermore, by recalling that CCS without restriction is not Turing complete and has decidable convergence, the present results have shaped an interesting expressiveness hierarchy, namely one in which HOCORE is strictly more expressive than HO<sup>-f</sup> (because of the discussion above), and in which HO<sup>-f</sup> is strictly more expressive than CCS without restriction.

Remarkably, we can easily extend our undecidability result proving that (weak) barbed bisimilarity is undecidable in a higher-order calculus obtained by extending  $\mathrm{Ho^{-f}}$  with restriction. Consider the encoding of Minsky machines used in Section 3 to prove the undecidability of convergence in  $\mathrm{Ho^{-f}}$ . Consider now the restriction operator  $(\nu\widetilde{x})$  used as a binder for the names in the tuple  $\widetilde{x}$ . Consider now a Minsky machine N (it is not restrictive to assume that it executes at least one increment instruction) and its encoding P, as defined in Definition 6. We have that N terminates if and only if  $(\nu\widetilde{x})P$  is (weakly) barbed equivalent to the process  $(\nu d)(\overline{d}\mid d\mid d.(\overline{w}\mid !w.\overline{w}))$ , where  $\widetilde{x}$  is the tuple of the names used by the encoding P excluding the name w.

**Related Work.** The most closely related work is [4], which was already discussed along the paper. We do not know of other works that study the expressiveness of higher-order calculi by restricting higher-order outputs. The recent work [12] studies finite-control fragments of Homer (a higher-order process calculus with locations). While we have focused on decidability of termination and convergence, the interest in [12] in on the decidability of barbed bisimilarity. One of the approaches explored in [12] is based on a type system that bounds the size of processes in terms of their syntactic components (e.g. the number of parallel components, location nesting). Although the restrictions such a type system imposes might be considered as similar in spirit to the limitation on outputs in Ho<sup>-f</sup> (in particular, location nesting resembles the output nesting Ho<sup>-f</sup> forbids), the fact that the synchronization discipline in Homer depends heavily on the structure of locations makes it difficult to establish a more detailed comparison between the works.

Also similar in spirit to our work, but in a slight different context, are some studies on the expressiveness (of fragments) of Ambient calculus [13]. Ambient calculi are related to higher-order calculi in that both allow the communication of objects with

complex structure. Some works on the expressiveness of fragments of Ambient calculi are similar to ours. In particular, in [14] it is determined that, for calculi without restriction (as Ho<sup>-f</sup> and Hocore), when the capability of ambient movement is ruled out from the language process termination is decidable for the fragment with recursion while it turns out to be decidable for the fragment with recursion. Hence, in [14] the separation between fragments comes from the source of infinite behavior considered, and not from the structures allowed in output action, as in our case. We find, however, that the connections between Ambient-like and higher-order calculi are rather loose, so a proper comparison is difficult also in this case.

**Future Work.** As already mentioned, a great deal of the expressive power in higher-order calculi resides in constructs for input and output. Here we have studied an alternative for limiting the output capabilities of the calculus; it would be interesting to understand if suitable limitations on input actions are possible, and whether they have influence on the expressiveness and decidability of higher-order calculi. Another interesting direction would be to compare higher-order and Ambient calculi from the expressiveness point of view.

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